ESP-WROOM-02(Deep-Sleep) Si7021 + OPiPC2 + 18650

-

Wi-Finnang2500000010Deep-Sleep0000000000000 nnnnSi7021nnnnWi-FinOPiPC2nnnnnnnnnnn × × □(https://www.calc-site.com/times/calc elapsed time) × 3000

□ESP-WR00M-02(Deep-Sleep) + Si7021 + 0PiPC2 + 18650 + Fan□

ESP-WROOM-02 + Si7021(SHT21)HTU21 GY-21) + Orange Pi PC2

HTU21

ESP-WR00M-02+SSD1306+BME280nnnnnnnnnnnnnnnnnnnnnnnnn

×

http://www.esp8266learning.com/esp8266-si7021-temperature-sens
or-example.php#codesyntax_1

[]____Wire.begin(); Wire.begin(5, 4); ______

×

×

https://neo-sahara.booth.pm/items/1924398

PIC32MM0064GPL028 + **SSD1306(GM009605)**

___XnView_____

[]ImageConverter (Mono)]C

_____**_**____**_**____**_**____**_**____

[crayon-6717490794782345649020/]

×

×

MPLABX

[crayon-671749079478d550300343/]

PIC32MM0064GPL028

Real Time Clock Module (DS1307 + AT24C32) + PIC18F2550 000000

[Real Time Clock Module (DS1307 + AT24C32) + PIC18F2550 + SC1602B____16×2 Character LCD with I2C Module __________

×

×

[]_____IchigoJam Clone([255]([]____])[]___IchigoJam[]][]([]___])
[]____[chigoJam[]][]([]___])
[]____[chigoJam[]][]([]___])[][chigoJam[]][]([]__])[][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][][chigoJam[]][[chigoJam[]][[chigoJam[]][[chigoJam[]][][chigoJam[]][][chigoJam[]][[chigoJam[]][[chigoJam[]][[chigoJam[]][][chigoJam[]][[

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Real Time Clock Module (DS1307 + AT24C32) + PIC18F2550 + SC1602B

___PIC18F2550____

[]_LCD_[]___ MPLAB C Compiler Libraries for PIC18 MCUs []____]
LCD Function []_____PIC16F84A + SC1602BS + XC8_[]____]
PIC18F2620[]_____RB0[RB3[LCD_[]____]
PIC18F2620[]_____RB0[RB3[LCD_[]___]
PIC18F2550[]
RB0[RB1[I2C]]SDA[SCL_[]__]
D___ICD Function []_____RD0[RD1]D____]

[crayon-67174907954e2296696474/]

×

if(set_count >= 8)

 $\square Real Time Clock Module (DS1307 + AT24C32) + PIC18F2550 <math display="inline">\square \square \square$

PIC16F1823 + LM61BIZ +

NJL7502L + I2C EEPROM

LM61BIZ × × × × ×

PIC16F1823 -> I2C(Bit Banging) -> AQM0802A and AT24C256B

EEPROM AT24C256B []]]

The device address word consists of a mandatory "1", "0" sequence for the first four most significant bits as shown. This is common to all two-wire EEPROM devices.

×

Only one 7-bit slave addresses (0111110) is reserved for the ST7032.

×

PIC16F1823 -> I2C(Bit Banging) -> AQM0802A

I2C____Bit banging_I2C___ LCD_____

×

Wikipedia

Bit banging is a technique for serial communications using software instead of dedicated hardware.

main.c[]InitCCT[][][][][][][][][InitPIC[][]] [crayon-67174907968e1753311718/] I2C.h[][][][][][][][][][][][]] [crayon-67174907968f2218202866/]



- ×
- ×
- ×

PIC12F675 --- I2C ---> AQM0802A [][][][Hello]

PIC12F675_____AQM0802A______ I2C____AQM0802A_____I2C___EEPROM______ I2C____AQM0802A______

[]___S_I2C_Start(); []P_I2C_Stop(); []____ACknowredge[]]]
[]____S_I2C_Start(); []P_I2C_Stop(); []_____ACknowredge[]]]